A DESIGN AND ANALYSIS OF COMPACT MICROSTRIP BANDPASS FILTER WITH INTEGRATED LNA FOR 0.8 TO 2.7 GHz

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ABSTRACT

This paper represents the design and analysis of the Microstrip wideband integrated low noise amplifier Bandpass filter for the frequency range of 0.8 GHz to 2.7 GHz. This frequency range is chosen for the design as most of the wireless applications work in this frequency range. The paper uses the compact design structure for designing the wideband filter from ref [5]. The filter has good performance in passband as well as in stopband of the filter. The filter has the compact size and higher selectivity of 0.923 and the input return loss below 10 dB and the output return loss less than 10 dB for the whole frequency range. The integrated low noise amplifier is from the analog devices ADL5544 which has the gain of 15 dB at 0.8GHz and the gain roll of 2 dB in the whole frequency band of the filter. The filter hardware is fabricated and tested with the network analyzer from Rohde & Schwarz model no ZVH8 which can measure from 100 KHz to 8 GHz. The simulated and the measured results are in good agreement with each other.

KEYWORDS

Bandpass filter (BPF), multiple-mode resonator (MMR), stepped impedance stub load resonator (SISLR), ultra wideband (UWB), Vector network Analyzer (VNA).

1. INTRODUCTION

In 2002 the Federal communication Commission(FCC) set the frequency range of the ultra-wideband (UWB) as the frequency from 3.1 GHz to 10.6 GHz. Since then the advancement in the development of various filters and couplers designed to work in this range started taking growth. Various designs of filters were designed like ring filters multi-mode resonators as shown in ref[1], [2], [3], [4]. The requirement slowly started shifting to the compactness with the good performance in the pass band as well in stopband. The various disadvantages of the Microstrip were considered like harmonic distortion Microstrip losses at higher frequencies etc. Later the multi-mode resonators were used which were able to give good performance in passband but smaller stopband ref [1], [2]. Various other techniques like defective ground were introduced later to have a wider stopband ref [6]. The actual motivation to design the band pass filter for any application is to save the time and the money required for the designing and fabricating the various filters for different applications which are running on different frequencies. By designing the Bandpass filter we actually cuts off the need to design various narrowband filter and use the same Bandpass filter for every application even if the application is running on different frequency. Just that the frequency of operation of the application should be in the passband of the Bandpass filter. Adding to this the gain of the Bandpass filter should be as maximum as possible.
also the input and output return loss of the filter should show some acceptable figures. Dealing with the performance of the filter we cannot ignore the other factors such as size and compactness of the design. As the advancement of technology is taking a mass growth the size will be the only major factor than others. Taking this into account the filter with compact structure is chosen for the frequency which has the maximum wireless applications.

In ref [5] the size of the filter was reduced improving the performance of the filter in the passband as well as in stopband. The size of the filter is reduced as compared to the filter designed in ref [4]. The Fig. 1 shows that the filter uses only a single SIS connected at the center of the uniform impedance transmission line and an aperture-backed beneath three inter-digital parallel coupled lines at connected at each side of the filter for coupling enhancement. The adopted method leads to a simplified objective function with a minimum number of variables to avoid convergence and implementation problems.

In ref [4], the filter size miniaturization was the major challenge faced by the design engineers so this paper proposed a Novel UWB Bandpass filter using stub load multiple mode resonator as shown in Fig. 2.
This filter had the filter size less as compared to the filter proposed in ref [3]. As shown in Fig.2 the filter used a uniform impedance resonator and consisted of the SIS (Stepped Impedance Stub) at the center and two extra added open stubs at the side of the center stub placed symmetrically around the center. The MMR consists of three open stubs in a uniform impedance resonator, and five modes, including two odd modes and three even modes within the desired band are combined to realize UWB passband.

In ref [3], the novel stepped impedance stub loaded resonator (SISLR) was proposed to design UWB BPF. The previously designed SIR type UWB BPF as shown in Fig.5 showed good performance in passband except the Stopbands suffer the slow increase in attenuation and there were no longer enough degree of freedom for effective control of resonant frequencies.

This resonator as shown in Fig.3 has more degrees of adjusting freedom to control its resonant frequencies, which results in conveniently relocating the required resonant modes within the UWB band. The basic structure of the proposed SISLR is shown in Fig.3. It consists of a traditional SIR with the characteristic admittance, and electrical lengths and, which is tapped-connected to a stepped-impedance stub (SIS) in the center. The SIS is also made of transmission-line sections of characteristic admittance, and electrical length. Since the SISLR is symmetrical in structure, odd- and even-mode analysis can be adopted to characterize it.
In ref [2] the Microstrip line stepped impedance stub loaded MMR was proposed in Fig.5. As discussed in ref [1], the first three resonant modes in the stepped-impedance MMR can be quasi-equally allocated within the concerned UWB passband by adjusting width/length ratios of central-to-side sections. However, this MMR-based filter usually suffers from a high insertion loss of about 2.0 dB in the upper UWB passband and a narrow upper stopband of 11.0 to 14.0 GHz. The former is mainly caused by parasitic radiation from the central part with wide strip conductor at high frequencies, while the latter is due to the 4th resonant mode in this stepped-impedance MMR. As shown in Fig.5, the proposed stub-loaded MMR is formed by properly attaching one single open-ended stub in the middle and two identical ones in the two symmetrical positions. The lengths of the central stub and side stubs are indicated by Lc and Ls, respectively. In this way, the first four resonant modes expect to be relocated within the UWB passband while pushing up the fifth mode to make up a wide upper stopband.

Compared with the conventional multi-mode resonator in ref [1], this filter design had an extra stepped-impedance stub loaded in the center. The performance of the filter was good but was large in size.

In ref [1], the initially proposed UWB Bandpass filter using a Microstrip line multiple-mode resonator (MMR) was presented as shown in Fig.6. Here the MMR has been properly modified in configuration so as to reallocate its first three resonant modes close to lower-end, center, and upper-end of the targeted UWB passband. Also, the coupling degree of the input/output parallel-coupled line sections is largely raised.

Fig.5 Configuration of the proposed UWB BPF based on stub-loaded MMR in ref [2]

Fig.6 Schematic of the compact Microstrip-line UWB Bandpass filter ref [1]
At the central frequency of the UWB passband, i.e., 6.85 GHz, the MMR consists of one half-wavelength low-impedance line section in the center and two identical $\lambda/4$ high-impedance line sections at the two sides. With respect to the configuration, the proposed MMR was categorized as a so-called stepped-impedance resonator (SIR). As a non-uniform transmission line resonator, the SIR was proposed in to enlarge the frequency spacing between the first and second-order resonant modes so as to effectively widen the upper stopband above the dominant passband of a Bandpass filter. Here, all the first three resonant modes are taken into account together and they are applied to make up a wide dominant passband. In this case, the first and third-order resonant frequencies basically determine the lower and upper cutoff frequencies of a wide passband. Further the two additional transmission poles in the $\lambda/4$ parallel-coupled lines, a UWB filter can be built up with good insertion and return loss in the entire passband of concern.

2. ANALYSIS AND DESIGN OF FILTER

The filter consists of a uniform transmission line of impedance $Z$ and has and electrical length of $2\theta_C$ and the characteristic admittance $Y_C$. In the structure of the filter as shown in Fig.7 the stepped impedance step is connected at the center of the uniform transmission line which has the characteristic impedance of $Z_A$ and $Z_B$ with the characteristic admittance as $Y_A$ and $Y_B$ with the electrical length of $\theta_A$ and $\theta_B$. The actual performance of the ideal filter should be that the gain of the filter should be $S_{21} = 1$ for the full passband of interest and then should have sharp cutoff after the pass band. This actually means that filter which is fed by the input impedance of 50 ohms is matched with the impedance of filter for the whole frequency band, where the input impedance of $Z_{in} = 50$ ohms and the output impedance of $Z_{out} = 50$ ohms. Now this condition can be well expressed through the given objective function.

$$F_{OBJECTIVE} = \sum_{f_{N_{min}}}^{f_{N_{max}}}(|Real(Z_{in} - 50)| + |Imag(Z_{in})|) < \varepsilon \quad [1]$$

Where $F_{N_{max}} = F_{max}/F_0$ and $F_{N_{min}} = F_{min}/F_0$. For the frequency of concern this objective function should be less than the convergence tolerance $\varepsilon$. The value of the convergence tolerance is taken as $\varepsilon = 10^{-4}$. The objective function mentioned above is very complicated as it consists of the real and the imaginary parts of the impedance of the filter. Simplification of the above function is required to solve the equation so the filter is divided in to two parts ref [5]. This can be shown in the Fig.8.

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![Fig.7 Basic structure of T shaped resonator with uniform transmission line and stepped impedance stub at the center.](image)
The actual goal of the simplification is to get the simple expressions of the admittances of $Y_m$ and $Y_n$ compared with the impedance $Z_{in}$ and to avoid forcing that their real parts should be equal. So therefore only the imaginary parts of $Y_m$ and $Y_n$ will compose of the objective function. As the real parts are same we have the expression

$$Y_n = Y_m + jB_{stub} \quad [2]$$

$$Y_n = G_m + jB_m + jB_{stub} \quad [3]$$

The matching at one port means the filter can be matched to any transverse plane of the line that has the characteristic admittance of $Y_c$ which divides the filter into two parts. Therefore the matching condition $Y_n = Y_m^*$ leads to

$$2B_m + jB_{stub} = 0 \quad [4]$$

Now from the ref [5] the various expressions for the susceptance $B_m$ as

$$B_m = \frac{y_c\tan\theta_c(1-y_p^2)}{1+y_p^2\tan^2\theta_c} \quad [5]$$

$$B_{stub} = 2Y_b \frac{k\tan\theta_a+\tan\theta_b}{1-k\tan\theta_a\tan\theta_b} \quad [6]$$

Where $Y_p = 1/(50Y_c)$ and $k = Y_a/Y_b$. To make sure that the filter gives the Bandpass response the condition of $2B_m + jB_{stub} = 0$ should be verified for every frequency within the bandwidth of the Bandpass filter. Therefore the new objective function can be formed as

$$F_{OBJECTIVE} = \sum_{f_{Nmin}}^{f_{Nmax}} \left| 2B_m + B_{stub} \right| \epsilon \quad (convergence tolerance) \quad [7]$$

Which can be simplified by substituting the equations in the objective function

![Fig.8 Simplified structure of the filter with their electrical lengths and admittance.](image-url)
Now to have the sharp selectivity at the cutoff frequency at the end of the passband of the filter it is necessary to design the center stub of the filter in such a way that the center stub will place the zeros on the frequency 0.8 GHz and 2.7 GHz which is the cutoff frequency of the filter. That actually means that the filter will have $|S_{21}| = 0$ at the frequency $f_{z1} = 0.8\text{GHz}$ and $f_{z2} = 2.7\text{GHz}$ are the two transmission zeros of the center stub also $B_{stub}(f_{z1}) = B_{stub}(f_{z2}) = \infty$ which could be designed at these frequencies as

$$ktan\theta_a(f_{z1}).tan\theta_b(f_{z1}) = 1 \quad i = (1,2) \quad [8]$$

Which leads to two expressions

$$\theta_{02} = \frac{f_0}{f_{z1}}tan^{-1}\left[\frac{1}{ktan(\theta_{01}(\frac{f_{z1}}{f_0}))}\right] \quad i = (1,2) \quad [9]$$

To find all possible combinations of $\theta_{02}, \theta_{01}$ and $k$ a numerical iterative method is necessary to verify the expression

$$|\theta_{02}(2)| - |\theta_{02}(1)| < \epsilon' \quad \text{Where} \quad \theta_{01} = 0 \quad [10]$$

The ratio of $\frac{f_{z2}}{f_{z1}}$ is a function of $\alpha = \frac{\theta_a}{\theta_b+\theta_a}$ for different values of $k$. The minimum value of $k$ can be obtained at $\theta_a = \theta_b = \theta$. So the value of $\alpha = 0.5$. In case where $\frac{f_{z2}}{f_{z1}} = 3.375$, then

$$\theta_x = \frac{\pi}{(\frac{f_{z2}}{f_{z1}}+1)}[10] \quad \theta_y = \pi - \theta_x \quad [11]$$

$K$ is less than the maximum value due to characteristic impedance value of the Microstrip line that vary from between 20 ohm to 140 ohm. So the $K$ varies from the minimum value to the maximum value of $k=7$.

3. DESIGN METHODOLOGY

This paper uses the structure of the Bandpass filter designed in ref [5]. The filter in ref[5] consist of a uniform transmission line with a stepped impedance open stub connected in the center of the line with the three inter-digitized parallel coupled lines at each side of the filter. The filter also uses the defective ground technique to have a better control over the input reflection coefficient of the filter. The filter designed in this paper is designed for the frequency range of 0.8 GHz to 2.7 GHz but the filter designed in ref[5] is designed for 3.1 to 10.6 GHz. The filter is designed in this paper uses the FR4 substrate having the dielectric constant of $\varepsilon_r = 4.6$ the substrate height of $h=1.6$ mm and conductor thickness of $T= 0.01$ mm.
The filter is then integrated with the Low noise amplifier from Analog Devices ADL5544. The amplifier has a good gain response in the frequency range of 0.1 GHz to 6 GHz. The amplifier is made stable in the frequency range by proper biasing the Amplifier by the biasing circuit. The filter and the amplifier circuit is separately designed and then integrated. The filter has a good response in the passband of the filter and sharp selectivity after the upper cutoff frequency. The selectivity factor of the filter is 0.9032 as the frequency at -3 dB is obtained as 2.8 GHz and frequency at -30dB is 3.100 GHz. The filter size is about 7 cm in length and 4 cm in height. This configuration of the filter is chosen as the filter has to be fabricated in India. The FR4 material of er=4.6 is easily available for fabrication. The minimum spacing between the tracks which can be fabricated in India is 0.2 mm. Hence no length or spacing between the tracks is not less than 0.2 mm. The layout of the filter is drawn using ADS layout window and momentum simulation is done to obtain the frequency response of the designed filter.

As shown in Fig.8 the filter consist of center stub and a uniform impedance line of λ/4 at the center frequency of 1.75 GHz. The center stub is connected in the center of the uniform impedance line and the lengths and the widths of center stubs are taken such that the zeros are placed at the cutoff frequencies of the filter. The inter-digital coupling is made on both side of the filter to suppress the frequencies after the cutoff frequency and to have a larger stopband.

Table 1. Comparison of the filter structures with their substrate and selectivity factor.

<table>
<thead>
<tr>
<th>Reference</th>
<th>Dielectric/substrate Height(mm)</th>
<th>SF</th>
</tr>
</thead>
<tbody>
<tr>
<td>[1]</td>
<td>10.8/1.27</td>
<td>0.642</td>
</tr>
<tr>
<td>[2]</td>
<td>10.8/1.27</td>
<td>0.594</td>
</tr>
<tr>
<td>[3]</td>
<td>2.55/0.8</td>
<td>0.926</td>
</tr>
<tr>
<td>[4]</td>
<td>2.55/0.8</td>
<td>0.921</td>
</tr>
<tr>
<td>[5]</td>
<td>2.33/0.5</td>
<td>0.861</td>
</tr>
<tr>
<td>This paper</td>
<td>4.6/1.6</td>
<td>0.9032</td>
</tr>
</tbody>
</table>

As shown in Fig.9 The layout of wideband Bandpass filter.
The filter also uses the defective ground structure in which the ground plane is not present below the coupling. The lengths of the defective ground can be obtained by the trial and error method. The defective ground structure minimizes the input return loss of the filter. The filter gain response shows that the filter has the passband from 0.8 GHz to 2.7 GHz. The filter has an insertion loss of about 0.5 dB.

![Fig.10 The output gain (S21) of wideband Bandpass filter.](image)

The Fig.9 and fig.10 shows the momentum simulation results of the filter. The filter shows its passband from 0.8 GHz to 2.7 GHz the S11 as shown in Fig.10 is below -10 dB for the whole passband of concern. The filter suffers from the second harmonics of the filter center frequency.

![Fig.11 The input return loss (S11) of wideband Bandpass filter.](image)
The stopband of the filter is extended up-to 4GHz. The designed filter uses the compact structure proposed in ref [5] hence is compact in size for the proposed frequencies of 0.8 GHz to 2.7 GHz. The size of the filter is only 7 cm. As the filter is designed for the low frequencies as compared to ref [5] the lengths of the filter are greater. The filter is designed to cover all the wireless applications which work in 800 MHz to 2.7 GHz band of frequencies.

![Fig.12 The output return loss (S22) of wideband Bandpass filter](image)

The output return loss of the simulated filter as shown in Fig.11 is less than -10dB for the whole passband of concern. The filter has the S22 of -10.038dB at 800 MHz and -13.096dB at 2.7 GHz and output return loss remains below -10dB between this band of frequency. The simulation result of output isolation of the filter is shown in Fig.12.

![Fig.13 The output isolation (S12) of wideband Bandpass filter.](image)
The output isolation of the filter is -9.367dB at 700 MHz and -8.560dB at 2.85 GHz. The filter has the good isolation in the stopband of the filter.

4. FILTER INTEGRATED WITH LNA

The filter designed in the section III is integrated with a low noise amplifier to further increase the gain of the signal filtered by the Bandpass filter. The low noise amplifier actually amplifies the signal obtained from the Bandpass filter and adds its self-submicron noise as minimum as possible. Low noise amplifier adds minimum noise to the circuit but amplifies the signal with the expected gain. Hence it is called as low noise amplifier. The low noise amplifier used in the design is from the analog devices ADL5544 which has good gain of 15dB in the range of 300 MHz to 2.7 GHz. The amplifier is made stable in 0.8GHz to 2.7 GHz by designing the biasing circuit of the amplifier. The biasing circuit of the amplifier can also be found in the data sheet of the amplifier. The stability of the amplifier is checked through Rollet’s criteria and Stab fact function in ADS schematic.

Figure 13 shows the integrated layout of the filter and the amplifier various different components for amplifier biasing are used like choking inductors and decoupling capacitors. The component padding and IC padding of the amplifier can be shown in the Fig.13. The IC padding in the layout is taken from the datasheet of the IC used. The inductors and the capacitor padding is taken according to the package size of the component e.g. 0603 or 5750. Now in comparison the 0603 package is small in size as compared to that of the 5750 package. The inductors and the capacitors are used from the Murata and Coilcraft. The small circles shown in the Fig.13 are Via which connects the upper or the top metal layer to the lower ground plane. The ground plane should be as large as possible in RF/Microwave circuits to minimize the radiation losses of the circuit otherwise the losses of the circuit increase and the signal travelling through Microstrip gets attenuated earlier due to excessive losses of radiation. The design of the filter is maintained to have minimum Microstrip losses.

5. FILTER HARDWARE RESULTS

The hardware results are tested on the VNA (Vector network analyzer) which can test the circuit up to 8 GHz. The dc voltage source is required for the amplifier biasing. The dc voltage source of 3 V is used for the amplifier ADL5544.
Figure 14 shows the measured gain $S_{21}$ of the designed filter integrated with amplifier. It is observed that the design has the gain of -30dB at 500 MHz then 13.91dB at 791 MHz then 13.20dB at 1.2165 GHz then 11.30dB at 2.246 GHz and 8.04dB at 2.71 GHz. The design has the gain roll-off of 2 dB in the passband of the filter.

Figure 15 shows the gain over the whole frequency range of 8GHz. The filter shows the sharp selectivity at cutoff frequencies of the filter. The filter has a good sharp attenuation in the stopband of the filter. The filter is affected with the second harmonics of the center frequency of the filter. Hence the gain plot rises after 4.5 GHz.

Figure 16 shows the measured gain $S_{21}$ of wideband Bandpass filter with integrated low noise amplifier tested on VNA for full frequency range of 8 GHz.
The Microstrip filters are all affected with the harmonics this is the disadvantage of Microstrip filter to have smaller Stopbands after the cutoff frequency. The input return loss of the measured filter as shown in Fig.16 is less than -8dB.

![Fig.16](image)

![Fig.17](image)

The input return loss (S11) of the measured wideband Bandpass filter integrated with amplifier. The filter measures the input return loss S11 of -11.8dB at 800 MHz, 7.82dB at 1.335 GHz, -12.86dB at 2.244 GHz and -14.72dB at 2.7165 GHz. For rest of the frequencies the input return loss stays below -10dB. The measured result of the filter are satisfactory as compared to the simulation result S11 of the filter.

![Fig.18](image)

The output return loss (S22) of the measured wideband Bandpass filter integrated with amplifier. Figure 17 shows the measured result of output return loss S22. The output return loss is less than -10dB for the whole passband of frequencies. The output return loss is measured as -18.61dB at 800 MHz, -11.36dB at 1.3125 GHz, -18.77dB at 2.244 GHz and -11.95dB at 2.7165 GHz.
The simulation and measured result of S22 are in good agreement with each other. Figure 18 shows the output isolation of the filter. The output isolation of the filter is below -20 dB for the whole frequency range of operation. The measured S12 has -21.21 dB at 800 MHz, -22.34 dB at 1.3125 GHz, -26.05 dB at 2.244 GHz and -31.89 dB at 2.7165 GHz. The measured and simulated results of S12 are satisfactory when compared to each other.

6. FABRICATION AND TESTING

As the design was to be fabricated in India the minimum spacing between the two tracks was not less than 0.2 mm. So the results were obtained keeping the minimum distance between tracks as 0.21 mm.
Figure 19 shows the PCB of the filter and amplifier IC assembled to the PCB with other components. The SMA connectors are connected to the input and the output ports so that the PCB can be tested on VNA (Vector network analyzer). The 2 pin connector is connected to supply the dc voltage to the amplifier IC. The filter is designed using the defective ground at the back of the inter-digital coupled lines from both the sides. Fig.20 shows the defective ground of the fabricated filter. The defective ground helps the designer to vary the coupling between the lines as per required. The lengths and the widths of the defective ground are taken by trial and error method.
The defective ground concept helps to improve the input/output return loss of the filter. Varying the length and the width too much can change the passband of the filter completely. The defective ground actually does not have a ground plane in that patch of the PCB due to which the coupling degree of the filter can be changed and varied. The length of the defective ground taken in this design is length=8mm and width =2.97 mm. This lengths and widths are found out by trial and error methods which can vary as per the frequency of operation. Fig.21 shows the testing setup for the fabricated filter. Testing was done on VNA from Rohde & Schwarz model no ZVH8 which can measure from 100 KHz to 8 GHz. The VNA is first calibrated correctly to open, shorts and 50 ohm terminations so that the testing results are obtained with accordance to the calibrated references.

7. CONCLUSION

The study has revealed that the design developed in the ref [5] is the best design in terms of the performance and size of the filter compared to the various other designs developed earlier. The filter is designed to operate at frequencies of 0.8 GHz to 2.7 GHz. The filter has the good performance in the pass band as well as in stopband. The input return loss of the simulated results of the filter is less than -10 dB in the entire passband and that in hardware result is less than -7dB. The filter suffers the maximum of only 0.5 dB of insertion loss. The gain S21 of the proposed design is constant in whole passband of having a gain roll-off of 2 dB in the passband of the filter. The output return loss S22 of simulated and the measured results is less than -10dB. The output isolation S12 of the design is less than -20dB. The selectivity factor of the proposed filter is about 0.9032 which is greater than the filter of ref [5]. The proposed filter is designed for the lower frequency then that of the ref [5] so the size of the proposed filter is greater than that of the filter designed in ref [5] as all the lengths of the filter depends on the wavelength of the center frequency of the band of operation of the filter. As the center frequency is much lower the wavelength is higher. Hence the lengths are higher. The filter uses the substrate of FR4 with the dielectric of $\varepsilon_r$=4.6 and height=1.6 mm and metal thickness of $t=0.01$mm. The filter has the sharper roll off with compact size as compared to the other filters in this frequency range. All the simulations are carried out by ADS software momentum simulation and the fabricated hardware is tested using the VNA (Vector network analyzer) and is observed that the hardware results are in good comparison with that of the simulated results.

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